

Figure 1: MPS Remultiplexer Module Block Diagram

FIG. 1

(1 of 6 IP Channels Shown)

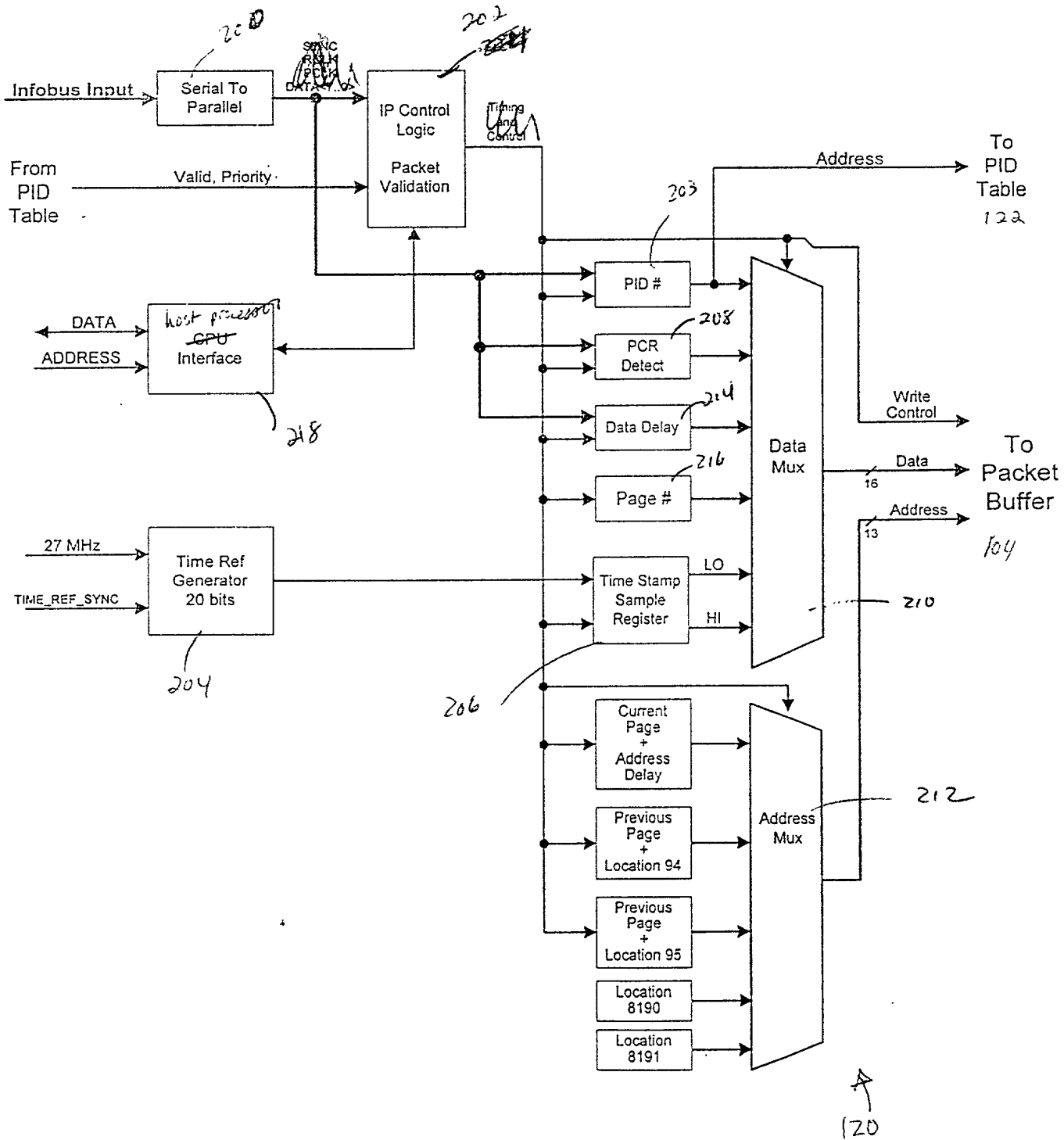


FIG. 2

Figure 5. Input PID Table

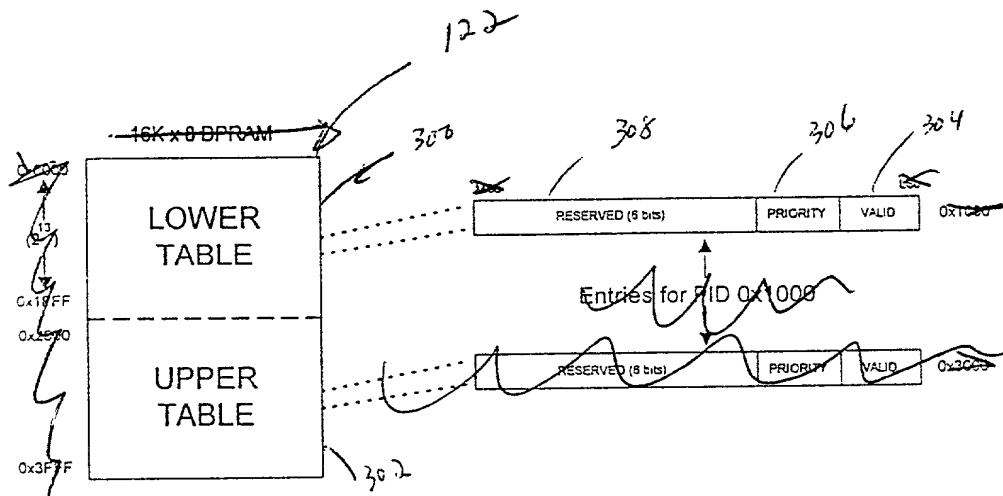


FIG. 3



Figure 10: Simplified ISR Flowchart

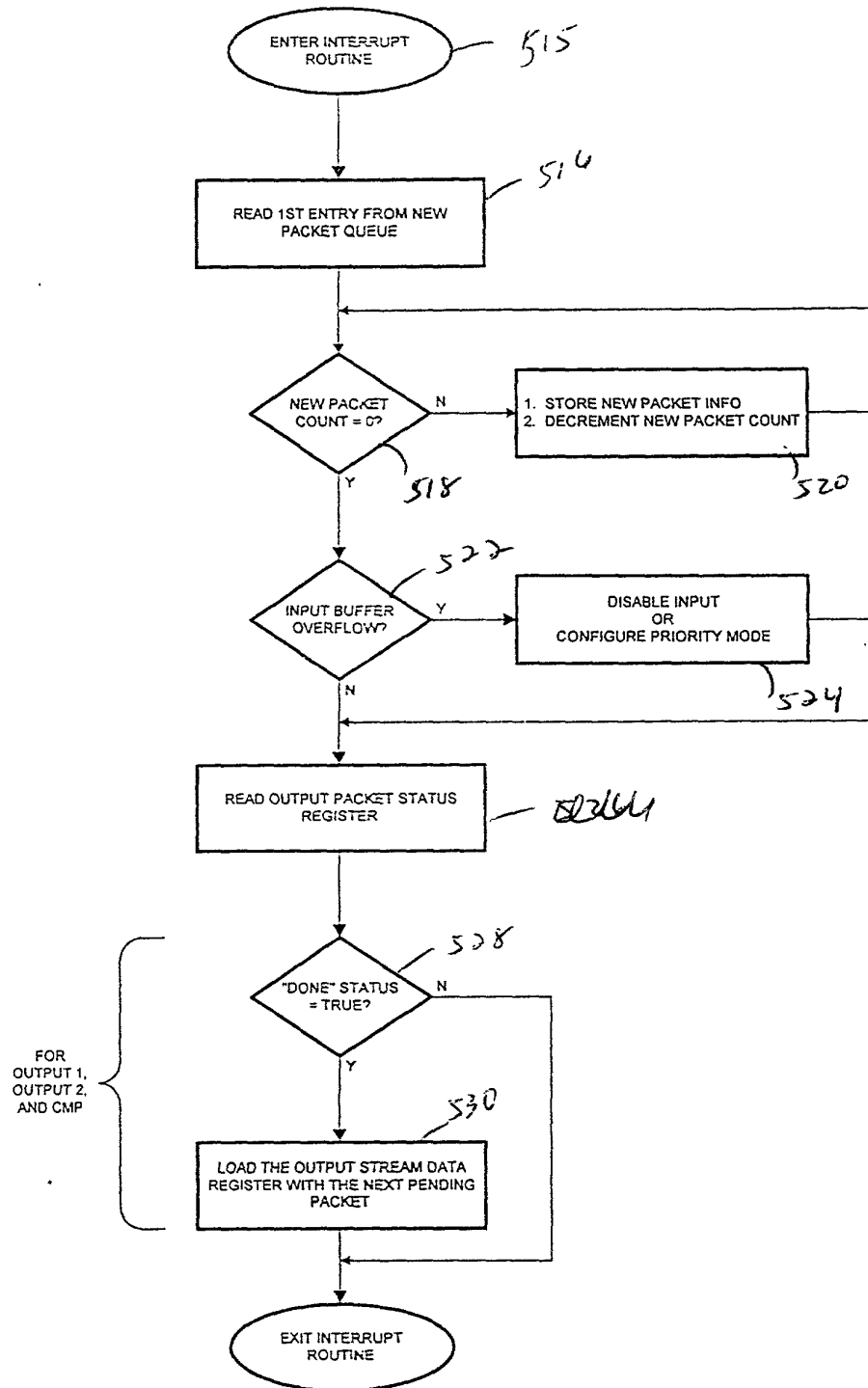


FIG. 5